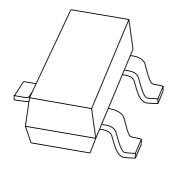
DISCRETE SEMICONDUCTORS

DATA SHEET



MMBT3904 NPN switching transistor

Product specification Supersedes data of 2002 Oct 04 2004 Feb 03





Philips Semiconductors

NPN switching transistor

MMBT3904

FEATURES

- Collector current capability I_C = 200 mA
- Collector-emitter voltage V_{CEO} = 40 V.

APPLICATIONS

· General switching and amplification.

DESCRIPTION

NPN switching transistor in a SOT23 plastic package. PNP complement: MMBT3906.

MARKING

TYPE NUMBER	MARKING CODE(1)
MMBT3904	7A*

Note

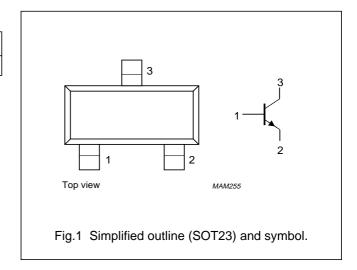
- 1. * = p: Made in Hong Kong.
 - * = t: Made in Malaysia.
 - * = W: Made in China.

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V _{CEO}	collector-emitter voltage	40	V
I _C	collector current (DC)	200	mA

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE NUMBER PACKAGE		PACKAGE	
ITPE NOWIBER			VERSION
MMBT3904	_	plastic surface mounted package; 3 leads SO	

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	60	V
V_{CEO}	collector-emitter voltage	open base	_	40	V
V _{EBO}	emitter-base voltage	open collector	_	6	V
I _C	collector current (DC)		_	200	mA
I _{CM}	peak collector current		_	200	mA
I _{BM}	peak base current		_	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

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CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

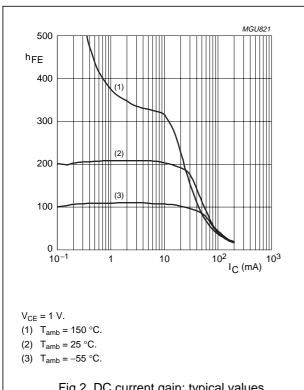
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 30 V	_	50	nA
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 6 V	_	50	nA
h _{FE}	DC current gain	V _{CE} = 1 V; see Fig.2; note 1			
		I _C = 0.1 mA	60	_	
		I _C = 1 mA	80	_	
		I _C = 10 mA	100	300	
		I _C = 50 mA	60	_	
		I _C = 100 mA	30	_	
V _{CEsat}	collector-emitter saturation	I _C = 10 mA; I _B = 1 mA	_	200	mV
	voltage	I _C = 50 mA; I _B = 5 mA	_	300	mV
V _{BEsat}	base-emitter saturation voltage	I _C = 10 mA; I _B = 1 mA	650	850	mV
		I _C = 50 mA; I _B = 5 mA	_	950	mV
C _c	collector capacitance	I _E = I _e = 0; V _{CB} = 5 V; f = 1 MHz	_	4	pF
C _e	emitter capacitance	$I_C = I_c = 0$; $V_{BE} = 500 \text{ mV}$; $f = 1 \text{ MHz}$	_	8	pF
f _T	transition frequency	I _C = 10 mA; V _{CE} = 20 V; f = 100 MHz	300	-	MHz
F	noise figure	I_C = 100 μA; V_{CE} = 5 V; R_S = 1 kΩ; f = 10 Hz to 15.7 kHz	_	5	dB
Switching ti	mes (between 10% and 90% lev	els); see Fig.3	•	•	
t _d	delay time	I _{Con} = 10 mA; I _{Bon} = 1 mA;	_	35	ns
t _r	rise time	I _{Boff} = −1 mA	_	35	ns
t _s	storage time		_	200	ns
t _f	fall time		_	50	ns

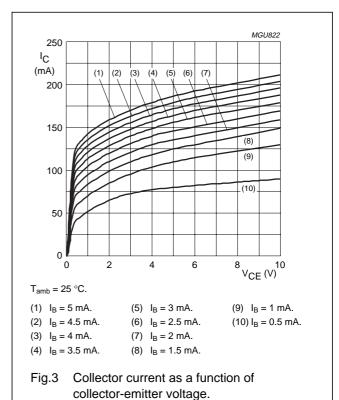
Note

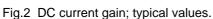
1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

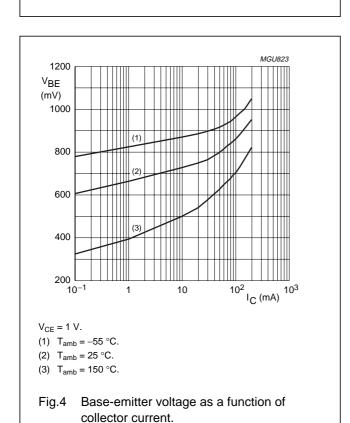
NPN switching transistor

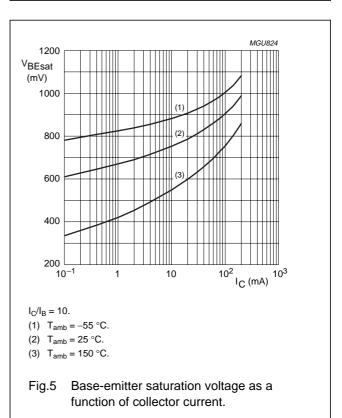
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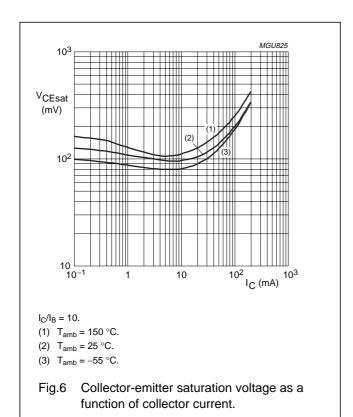




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NPN switching transistor

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oscilloscope $\frac{(\text{probe})}{450\,\Omega}$ $\frac{\text{V}_{\text{CC}}}{450\,\Omega}$ oscilloscope $\frac{(\text{probe})}{450\,\Omega}$ osci

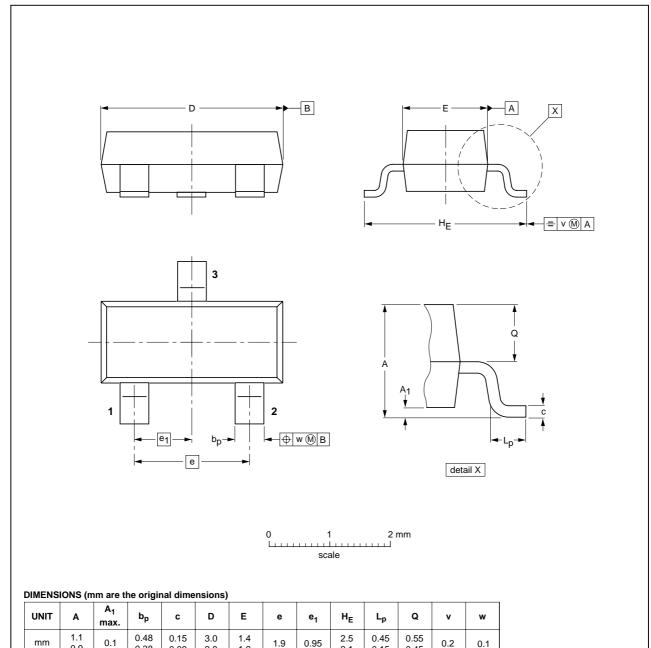
NPN switching transistor

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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



ITLINE REFERENCES			EUROPEAN	ISSUE DATE	
IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
	TO-236AB				97-02-28 99-09-13
_	IEC	IEC JEDEC	IEC JEDEC EIAJ	IEC JEDEC EIAJ	IEC JEDEC EIAJ PROJECTION

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DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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Notes

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- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

DEFINITIONS

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